

B / ERW



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q60098

Yoko HORIGUCHI

Appln. No.: 09/615,705

Group Art Unit: 2811

Confirmation No.: 4487

Examiner: Ori NADAV

Filed: July 13, 2000

For: SEMICONDUCTOR INTEGRATED CIRCUIT

STATEMENT OF SUBSTANCE OF INTERVIEW

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please review and enter the following remarks summarizing the interview conducted on
May 5, 2005:

REMARKS

An Examiner's Interview Summary Record (PTO-413) was attached with the Notice of
Allowance dated May 11, 2005.

The interview was initiated by the Examiner. Therefore, no further recordation by the
Applicant is believed to be required.

During the interview, the following was discussed:

1. Brief description of exhibits or demonstration: NONE
2. Identification of claims discussed: Claim 21.
3. Identification of art discussed: NONE

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4. Identification of principal proposed amendments: Claim 21, line 11, after "wire resistance", insert ---R1---. Claim 21, line 12, after "wire resistance", insert ---R2---.

5. Brief Identification of principal arguments: NONE

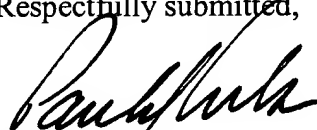
6. Indication of other pertinent matters discussed: NONE

7. Results of Interview: Applicant's undersigned representative authorized the amendment to Claim 21 as proposed by Examiner Nadav.

It is respectfully submitted that the instant STATEMENT OF SUBSTANCE OF INTERVIEW complies with the requirements of 37 C.F.R. §§1.2 and 1.133 and MPEP §713.04.

It is believed that no petition or fee is required. However, if the USPTO deems otherwise, Applicant hereby petitions for any extension of time which may be required to maintain the pendency of this case, and any required fee, except for the Issue Fee, for such extension is to be charged to Deposit Account No. 19-4880.

Respectfully submitted,



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WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: June 13, 2005